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Substitute for form 1449A/PTO				Complete If Known	
				Application Number	10/812080
				Filing Date	March 30, 2004
				First Named Inventor	Masataka KATO
				Art Unit	2824
				Examiner Name	V. LE
Sheet	1	of	2	Attorney Docket Number	500.32256C12

U.S. PATENT DOCUMENTS						
Examiner Initials'	Cite No. <sup>1</sup>	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (If known)	Number-Kind Code <sup>3</sup> (If known)			
		US- 4,924,437		05/08/1990	Paterson et al	
		US- 4,953,129		08/28/1990	Kobayashi et al	
		US- 5,053,648		10/01/1991	van der Elshout et al	
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Nakamura et al

## FOREIGN PATENT DOCUMENTS

Examiner Initials'	Cite No. <sup>1</sup>	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>4</sup>
		Country Code <sup>5</sup> -Number <sup>6</sup> -Kind Code <sup>7</sup> (If known)	Number-Kind Code <sup>8</sup> (If known)				
JK		JP 62-276878		12/1987			
		JP 04-014871		01/1992			
		JP 03-218496		09/1991			
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		JP 62-288098		12-25-1987	S. Nabeya et al		AB

Examiner Signature	Vu Le	Date Considered	9/17/04
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Substitute for form 1449B/PTO				<b>Complete If Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				<b>Application Number</b>	10/812080
				<b>Filing Date</b>	March 30, 2004
				<b>First Named Inventor</b>	Masataka KATO et al
				<b>Group Art Unit</b>	2824
				<b>Examiner Name</b>	V.I.E.
				<b>Attorney Docket Number</b>	500.32256C 12
Sheet	2	of	2		

#### **OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), and/or city and/or country where published	T <sup>2</sup>
JY		<i>IEEE Journal of Solid State Circuits</i> , Vol. 28, No. 4, April 1999, pp. 484-491.	
JY		<i>IEEE Journal of Solid State Circuits</i> , Vol. SC-17, No. 5, Oct. 1982, pp. 821-827.	
JU		KUME et al, H., "A 1.28 $\mu\text{m}^2$ Contactless Memory Cell Technology for a 3-V Only 64 Mbit EEPROM", <i>International Electron Devices Meeting</i> , 1992, <i>Technical Digest</i> , pp. 24.7.1-24.7.3.	
JY		Nakayama et al, T.. "A 60ns 16Mb Flash EEPROM with Program and Erase Sequence Controller", 1991 IEEE ISSCC, pp. 260-261.	

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